



Patent

Docket No.: TRAN-P059

Information Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	08/03/04	Name of Person Making the Deposit:	ANTHONY CHOU
		Signature of the Person Making the Deposit:	<i>Anthony Chou</i>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Andrew Read, Sameer Halapete and
Keith Klayman

Group Art Unit:

RECEIVED

Filed: 10/23/00

Examiner:

AUG 11 2004

Application No.: 09/694,433

Technology Center 2100

Title: SAVING POWER WHEN IN OR ON TRANSITIONING TO A STATIC MODE OF A PROCESSOR
(AS AMENDED)The Commissioner of Patents and Trademarks
Washington, D.C. 20231
Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

- Formal drawings, totaling sheets.
..... Informal drawings, totaling sheets.
..... Certification for PTO Consideration
☒ Information Disclosure statement (1 sheet)
..... Information Disclosure statement and late filing fee
☒ Form 1449
..... Petition for Extension of Time
☒ Other: References

Fee Calculation (for other than a small entity)					
Fee Items				Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)				\$.00	\$0.00
Information Disclosure Statement, late filing				\$180.00	\$0.00
Other:					\$0.00
Total Fees					\$0.00

PAYMENT OF FEES

- The full fee due in connection with this communication is provided as follows:
 - ☒ The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
 - ☐ A check in the amount of \$0.00
 - ☐ Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060
Customer No: 45590

Respectfully submitted,

Date: 8/3/04

By: Ronald M. Pomeroy
Ronald M. Pomeroy
Reg. No. 43,009



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: TRAN-P059

Inventor(s): Andrew Read, Sameer Halapete and
Keith Klayman

Group Art Unit:

RECEIVED

AUG 11 2004

Filed: 10/23/00

Examiner:

Technology Center 2100

Serial No.: 09/694,433

Title: SAVING POWER WHEN IN OR ON TRANSITIONING TO A STATIC MODE OF A PROCESSOR
(AS AMENDED)

The Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
5,832,205	MEMORY CONTROLLER FOR A MICROPROCESSOR FOR DETECTING A FAILURE OF SPECULATION ON THE PHYSICAL NATURE OF A COMPONENT BEING ADDRESSED	11/03/98

The Examiner's attention is respectfully directed to the following document:

Intel Corporation; "Intel 82801 CAM I/O CONTROLLER HUB (ICH3-M)" Datasheet; July 2001

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060
Customer Number: 45590

Respectfully submitted,

Date: 8/3/04

By: Ronald M. Pomeroy
Ronald M. Pomeroy
Reg. No. 43,009



Attorney Docket No.: TRAN-P059

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Patent Application

RECEIVED

AUG 11 2004

Technology Center 2100

Inventor(s): Andrew Read, Sameer Halapete and Keith Klayman Group Art Unit:

Filed: 10/23/00

Examiner:

Serial No.: 09/694,433

Title: SAVING POWER WHEN IN OR ON TRANSITIONING TO A STATIC MODE OF A PROCESSOR
(AS AMENDED)

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	5,832,205	11/03/98	Kelly et al.	395	185.06	08/20/96
	B						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	C							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	D	Intel Corporation; "Intel 82801 CAM I/O CONTROLLER HUB (ICH3-M)" Datasheet; July 2001
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.